



**DEFENSE LOGISTICS AGENCY**  
DEFENSE SUPPLY CENTER, COLUMBUS  
POST OFFICE BOX 3990  
COLUMBUS, OH 43216-5000

IN REPLY  
REFER TO

DSCC-VQ (VQC-04-005576/Mr. Tran/614-692-0606/mjg)

JAN 29 2004

SUBJECT: Level Q and N Certification, MIL-PRF-38535, FSC 5962

Mr. John East  
President, CEO  
Actel Corporation  
2061 Stierlin Court  
Mountain View CA, 94043-4655

Dear Mr. East:

Actel Corporation has demonstrated to the Defense Supply Center, Columbus (DSCC) that it complies with MIL-PRF-38535, the Performance Specification used by the Department of Defense for Monolithic Integrated Circuits.

Actel Corporation is granted full Q and N level certification, effective January 22, 2004, for the technology flows used for the quality assurance levels listed in the enclosure and as documented on the Quality Management Plan (QMP) document # C-00-QMP revision 13, dated July 15, 2003. This certification and listing includes the subcontractors approved by Actel Corporation. This letter supersedes DSCC letters (VQC-99-037 and VQC-98-077) to reflect the current certification status of Actel Corporation.

In addition, the parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow Actel Corporation to mark parts with "Q" or "QML". These designators have been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., One which allows less government oversight), the use of world-wide commercial production lines, reduced non-value added product testing based on statistical process control (SPC), and other cost advantages.

Testing must be performed using facilities and methods listed in the Laboratory Suitability letter (VQC-04-005585), or at facilities approved by Actel Corporation's Technical Review Board using its MIL-PRF-38535 Quality Management Program Plan.

This Certification is subject to the conditions in DoD 4120.3-M, Defense Standardization Program.

Any and all facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities. Offshore facilities are subject to all of the conditions of MIL-PRF-38535, Appendix E.



In addition, it is requested that the following activities be reported promptly to DSCC:

- Changes to certified facilities; process flows, or approved testing subcontractors
- Problem evaluation and a corrective action when:
  - a. A technology conformance inspection (TCI) failure has been validated
  - b. The reliability of shipped product is questionable
- Test optimization, including:
  - a. Implementation – paragraph J.3.12, Appendix J, MIL-PRF-38535
  - b. Changing, suspending or canceling a prior test optimization.
- Additions or deletions of parts in the QML-38535
- Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the Qualifying Activity. IF warranted, it may be withdrawn by this Center at any time.

If you have any questions, please contact Mr. Vinh Tran at (614) 692-0606.

Sincerely,



ROBERT P. EVANS

Chief

Sourcing and Qualifications Unit

Enclosure

cc:

DSCC-VQC (Michael Grammens)

DSCC-VQC (Scott Thomas)

Actel (Mrs. Nawal Cotran)

Actel (Mr. Craig Taylor)

<b>Operation</b>	<b>Facility</b>	<b>Location</b>	<b>Technology</b>
Design	Actel Corporation	2061 Stierlin Court, Mountain View CA, 94043-4655, USA.	CMOS, ONO, Antifuse, Flash, based - Field Programmable Gate Array (FPGA)
<b>Wafer Fabrication</b>			
	BAe Systems Formerly (Lockheed Martin)	9300 Wellington Road, Manassas, VA 20110	<b>Wafer Fabrication, Assembly and Test</b> of 0.8 $\mu$ CMOS Rad Hard Field Programmable Gate Arrays - Wafer Probe
	Matsushita Electronics Corp (MEC)	1 Koutari Yakemachi Nagaokakyo City Kyoto Japan	<b>Wafer Fabrication</b> - 0.25 $\mu$ , 0.6 $\mu$ , 0.8 $\mu$ and 1.0 $\mu$ CMOS Field Programmable Gate Arrays- Wafer Probe
	Chartered Semiconductor Manufacturing Inc. (CSM)	No. 2 Science Park Drive, Singapore Science Park, Singapore 118222	<b>Wafer Fabrication</b> - 0.35 $\mu$ , 0.45 $\mu$ , 0.6 $\mu$ , CMOS Field Programmable Gate Arrays- Wafer Probe
	Winbond Electronics Corp.	No. 4, Creation Road III, Science-based Industrial Park, Hsinchu, Taiwan, ROC.	<b>Wafer Fabrication</b> - 0.45 $\mu$ , 0.6 $\mu$ , 0.8 $\mu$ CMOS Field Programmable Gate Arrays- Wafer Probe
	United Microelectronics Corp. (UMC)	No. 3, Li-Hsin 2nd Road, Science-Based Industrial Park, Hsinchu City, Taiwan, R.O.C.	<b>Wafer Fabrication</b> - 0.15 $\mu$ , 0.22 $\mu$ , CMOS and 0.22 $\mu$ Flash/CMOS Field Programmable Gate Arrays- Wafer Probe
	Infineon Technologies AG	Postfach 100944, 93009 RBG Wernerwerkstrasse 2 D-93049 Regensburg, Germany	<b>Wafer Fabrication</b> - 0.25 $\mu$ Flash/CMOS Field Programmable Gate Arrays- Wafer Probe

Assembly			
	Amkor Technology Philippines (AAPF)	KM-22 East Service Road So. SuperHighway Muntinglupa, Philippines	Hermetic package assembly for Ceramic Quad Flatpak (CQFP) and Pin Grid Array (CPGA) packages
	Kyocera America, Inc.	8611 Balboa Avenue San Diego, CA 92123	Hermetic package assembly for Ceramic Quad Flatpak (CQFP) and Pin Grid Array (CPGA) packages
	Six Sigma	905 Montague Expressway Milpitas, CA 95035	Solderability (Dipping, Testing, Inspection) Assembly

**Current SMD listing**

See Attachment