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AVNET MEMEC RELEASES ACTEL PROASIC3 EVALUATION KIT

Phoenix, AZ –Avnet Memec, a division of Avnet Electronics Marketing Americas, today announced the release of its Actel ProASIC3 Evaluation Kit, the only available solution that enables product development and evaluation using the highest density ProASIC3 field-programmable gate array (FPGA). Featuring a single-chip, one-million gate A3P1000 device in a 484-pin FBGA package, the new kit provides customers with a low-cost solution to validate the performance of the high-density, kit-compatible A3P400, A3P600 and A3P1000 ProASIC3 devices. For designers of power-sensitive portable applications, the kit also enables quick prototyping of the new flash-based Actel IGLOO family, the industry's lowest power FPGAs.

Avnet Memec's new evaluation kit allows flexible implementation options for the up to 288 user I/Os, including high-speed, single-ended and differential pairs, and provides more than 50 uncommitted general-purpose I/Os brought to header pins. Additionally, expansion bus signal integrity is maintained by using high-speed Q-strip connectors that support daughter cards designed to Avnet's EXP™ expansion module [specification](#).

The Actel ProASIC3 Evaluation Kit contains a single-chip A3P1000 FPGA, programming cable, power supply, tutorials and support documentation in addition to the board. Additional support is available from Actel, including a high-speed FlashPro3 programmer, which interfaces to a PC via a USB 2.0 or USB 1.1 interface, and Actel's Libero Integrated Design Environment (IDE) software, which can be downloaded from Actel's Web site at www.actel.com.

“Since its introduction, Actel's ProASIC3 family has seen great success in the market. With the continued global interest in flash-based FPGAs and the recent introduction of Actel IGLOO, the roll out of Avnet's ProASIC3 Evaluation Kit is a timely one,” said Martin Mason, director of silicon marketing, Actel. “The combination of Actel's low-cost, high-density ProASIC3 FPGAs and a customized development kit enables designers to reduce time to market without compromising on cost or performance.”

“Increasingly, electronics OEMs are utilizing higher density FPGAs to save board real estate while simultaneously delivering more functionality,” said Rafael Cruz, vice president of design services, for Avnet Electronics Marketing Americas. “With the Actel ProASIC3 Evaluation Kit, Avnet Memec is expanding options, including density and pin count, for designers using flash-based FPGAs.”

The kit is available for immediate purchase with delivery in mid-October from Avnet Memec for \$199. Additionally, Avnet Memec also provides world-class value-added technical support and tools for the Actel portfolio. For more information, please visit <http://em.avnet.com/proasic3evalkit>.

About Avnet Electronics Marketing

Avnet Electronics Marketing is the largest operating group of Phoenix-based Avnet Inc. (NYSE:AVT), a Fortune 500 company. Avnet Electronics Marketing serves electronic original equipment manufacturers (EOEMs) and electronic manufacturing services (EMS) providers in 68 countries, distributing electronic components from leading manufacturers and providing associated design-chain and supply-chain services. The group's Web site is located at <http://www.em.avnet.com>.

Avnet markets, adds value to, and distributes the products of the world's leading electronic component suppliers, computer manufacturers and embedded subsystem providers. The company also offers supply- and engineering-design-chain services. For the fiscal year ended July 1, 2006, Avnet generated revenue of \$14.25 billion. Visit www.avnet.com/.