

DOULOS ANNOUNCES ‘GET SMART’ TRAINING FOR ACTEL’S SMARTFUSION MIXED SIGNAL FPGAs

Doulos Delivers Training Tuned for Embedded Developers Using Actel’s New SmartFusion Devices

Nuremberg, Germany, March 2, 2010 — Doulos announces training support for Actel Corporation’s (NASDAQ: ACTL) new SmartFusion™ intelligent mixed signal FPGAs and Libero® 9.0 Integrated Design Environment (IDE) and development environment adding the vital component of design know-how to ensure designers can turn their ideas into reality.

The combination of three programmable elements – FPGA, ARM® Cortex™-M3-based microcontroller subsystem and analogue components – makes the SmartFusion™ family a fully-customizable, easy-to-use platform for system designers. But using this platform also requires a solid know-how of the platform components used and the design methodologies required.

Building on years of experience in design training for Actel’s FPGA products as well as for ARM cores, in collaboration with Actel, Doulos has developed a customised training program to address the needs of developers using SmartFusion mixed signal FPGAs.

Actel’s Sr. Manager, Design Solutions Marketing and Training, Wendy Lockhart, comments on the partnership with Doulos, “We are happy to have Doulos as a very competent and highly qualified training and support partner for SmartFusion. It will help our customers very quickly adopt this new and exciting technology to considerably shorten their time-to-market.”

Jens Stapelfeldt, Product Manager for Doulos for ARM related business, explains, “As the leading global training provider for ARM-based system designs for over 10 years – and the number one player for FPGA training in Europe, we are well positioned to support customers looking to exploit Actel’s new SmartFusion devices. Our existing ARM and FPGA related training covers many of the requirements and we will be adding other important Embedded Systems and analog training modules in the coming months.”

Doulos’ support for SmartFusion designers will take the form of RapidGain events – intensive 1 or 2 day hands-on workshops offered directly with Actel and ARM partners and channels – as well as comprehensive project-ready 4-5 day training classes scheduled across Europe and the US. In addition, standard or customized on-site classes can be provided worldwide for interested companies. For more information see www.Doulos.com/Actel.

About SmartFusion Mixed Signal FPGAs

Combining a high performance FPGA, an ARM® Cortex™-M3 microcontroller subsystem and programmable analogue on a single chip, SmartFusion mixed signal FPGAs provide embedded designers with full customisation, unparalleled IP protection and ease of use.

About Doulos

Doulos is the global leader for the development and delivery of market leading training solutions for Embedded Systems, FPGA and ASIC design and verification. Our business ethos is 'service through excellence' which, when combined with our world-leading independent know-how, makes Doulos the ideal training partner.

Doulos is an ARM Approved Training Center since 2002, and delivers ARM architecture training in the context of deeply embedded systems (ASIC SoC design) as well as ARM-processor-based microcontrollers.

Doulos is the only ARM approved training partner covering the complete design flow and know-how with a full scope supporting training portfolio. Doulos is the only ARM service partner with a fully independent background in the EDA industry, covering the design of SoCs and SoPCs as well.

Learn more at www.doulos.com.

About Actel

Actel is the leader in low-power FPGAs and mixed-signal FPGAs, offering the most comprehensive portfolio of system and power management solutions. Learn more at www.actel.com.