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*****Webcast Alert*****

ARM Cortex-M1 Embedded Processor Design Flow for Actel Nonvolatile FPGAs

TOPIC: ARM[®] Cortex-M1 Embedded Processor Design Flow for Actel Nonvolatile FPGAs.

Webcast will feature:

- Embedded processor design flow, highlighting Libero[®] Integrated Development Environment (IDE) and SoftConsole
- SmartDesign for design entry and building SoC designs
- Free industry-standard embedded processor offerings including the ARM Cortex-M1 and Core8051s
- Low-power IGLOO[®] FPGAs and Fusion[®] mixed-signal FPGAs

View now at: www.actel.com/support/webcasts/default.aspx?f=processor_designflow

For more information on low-power IGLOO FPGAs visit: www.actel.com/products/iglooseries

For more information on Fusion mixed-signal FPGAs visit: www.actel.com/products/fusion

About Actel

Actel is the leader in low-power FPGAs and mixed-signal FPGAs, offering the most comprehensive portfolio of system and power management solutions. Power Matters. Learn more at www.actel.com.

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